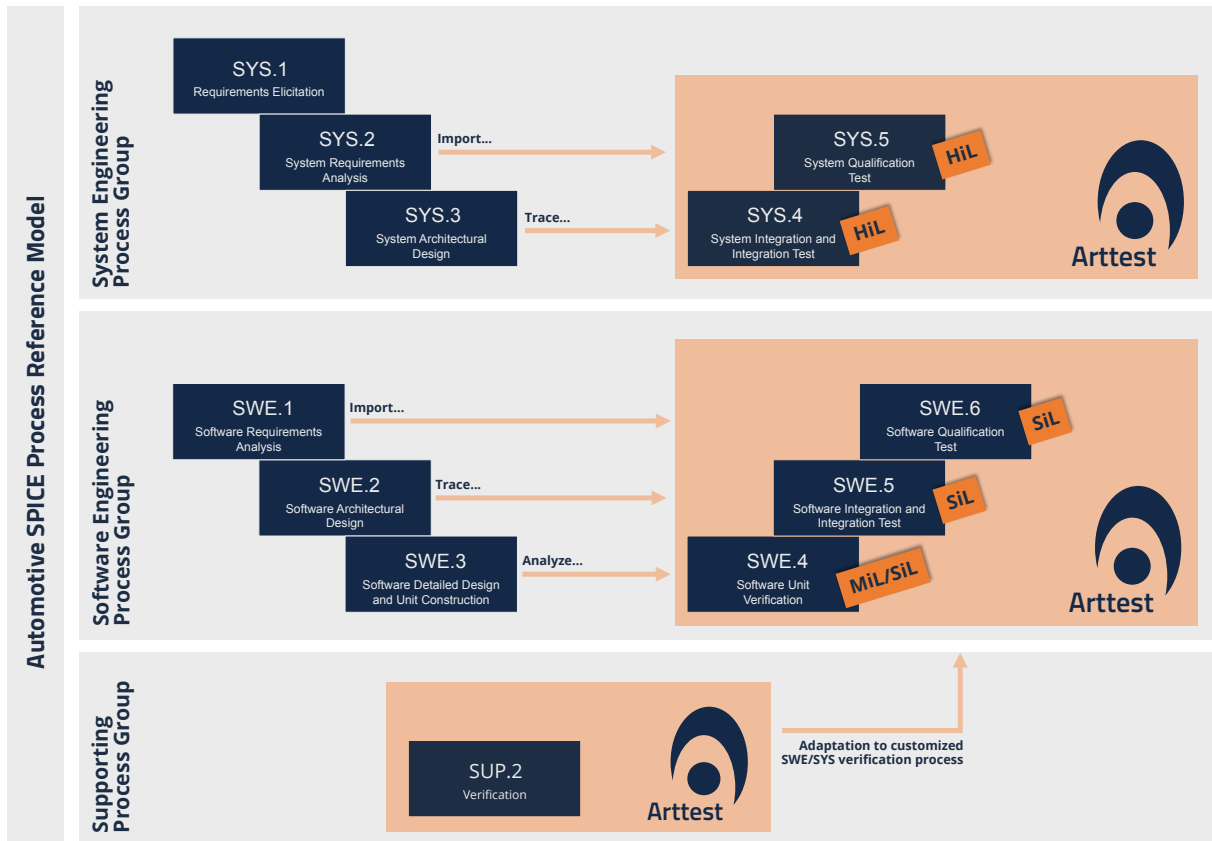


# Arttest in Automotive SPICE Compliant Processes



Arttest in the Automotive SPICE Process Reference Model

## Embedded Systems Verification Activities

Arttest is a comprehensive and powerful tool for embedded systems testing activities. It is tailored to the signal-based paradigm of embedded systems' interfaces and interactions and can thus be used in the context of all ASPICE processes that involve embedded systems' software and/or hardware verification.

## Technology-Agnostic Platform

Arttest's technology-agnostic platform design can interface with the market-leaders in MiL/SiL /HiL development like MATLAB/Simulink and dSPACE SCALEXIO as well as with a wide variety of other platforms from PLCs to FPGAs to custom hard-/software solutions to handwritten C-code.

## Automotive SPICE Performance Indicators

### ...on the example of SWE.4

With Arttest, you can achieve all five *process outcomes* specified as the SWE.4 ("Software Unit Verification") performance indicators: The verification strategy based on test specifications that allows for reproducible regression testing; verification criteria in terms of reference signals, tolerances and assertions; automated generation and execution of test cases from test specifications with data recording, logging and evaluation; bidirectional traceability between tests and software units as well as between tests and test results; export and distribution functionalities that are well-applicable in the context of CI/CD.

For more information or to receive a quote, contact us at [salesdesk@logic.nl](mailto:salesdesk@logic.nl) or by telephone: +31 (0)77 3078438



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